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TITLE: NT156FHM-N41
Product Specification
Rev. O

Chongqing BOE Optoelectronics Technology Co., Ltd

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	TFT-LCD		2016.01.12	1 <b>OF</b> 32



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# REVISION HISTORY

()preliminary specification

 $(\sqrt{\phantom{a}})$ Final specification

Revision No.	Page	Description of changes	Date	Prepared
PO	32	Initial Release	2016.01.12	胡伟

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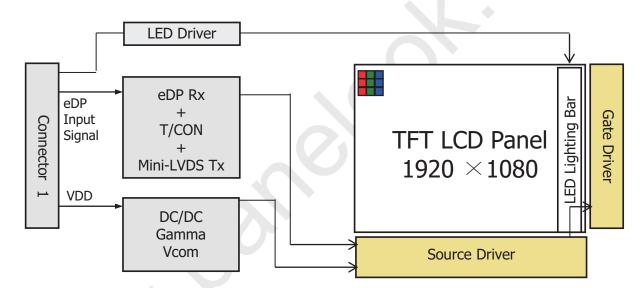
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## 1.0 GENERAL DESCRIPTION

#### 1.1 Introduction

NT156FHM-N41 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 15.6 inch diagonally measured active area with Full-HD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical Stripe and this module can display 262,144 colors. The TFT-LCD panel used for this module is a low reflection and higher color type. Therefore, this module is suitable for Notebook PC. The LED Driver for back-light driving is built in this model. All input signals are eDP1.2 interface compatible.



### 1.2 Features

- 2 lane eDP Interface with 2.7Gbps Link Rates
- Thin and light weight
- 6-bit color depth
- Single LED Lighting Bar. (Bottom side/Horizontal Direction)
- Data enable signal mode
- Side Mounting Frame
- Green Product (RoHS & Halogen free product)
- On board LED Driving circuit
- Low driving voltage and low power consumption
- On board EDID chip

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# 1.3 Application

Notebook PC (Wide type)

### 1.4 General Specification

The followings are general specifications at the model NT156FHM-N41. (listed in Table 1.)

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	344.16(H) ×193.59(V)	mm	
Number of pixels	1920 (H) ×1080 (V)	pixels	
Pixel pitch	0.17925(H) × 0.17925(V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	6bit		
Display mode	Normally White		
Dimensional outline	359.5 (H)×206.5(V)×3.2 (D:max)	mm	
Weight	370(max)	g	
Surface treatment	Anti-Glare / Hard coating		
Back-light	Bottom edge side, 1-LED Lighting Bar type		Note 1
Power consumption	P <sub>D</sub> : 0.8	W	Note 2
	P <sub>BL</sub> : 2.6	W	
	P <sub>total</sub> : 3.4	W	

Notes: 1. LED Lighting Bar (36\*LED Array)

Notes: 2. Maximum Measurement Condition : Mosaic Pattern

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# 2.0 ABSOLUTE MAXIMUM RATINGS

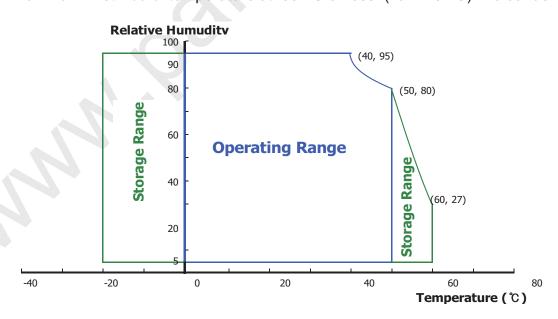
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings>

Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V <sub>DD</sub>	-0.3	4.0	V	Note 1
Logic Supply Voltage	V <sub>IN</sub>	V <sub>ss</sub> -0.3	V <sub>DD</sub> +0.3	V	Note 1
Operating Temperature	T <sub>OP</sub>	0	+50	${\mathbb C}$	Note 2
Storage Temperature	T <sub>ST</sub>	-20	+60	$^{\circ}$	Note 2

- Notes: 1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
  - Temperature and relative humidity range are shown in the figure below.
     RH Max. (40 °C ≥ Ta)
     Maximum wet bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.



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# 3.0 ELECTRICAL SPECIFICATIONS

### 3.1 Electrical Specifications

< Table 3. Electrical specifications >

Ta=25+/-2°C

Davameter		Min	Tura	Max	Heit	Damarka
Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V <sub>DD</sub>	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	$V_{RF}$	ı	ı	100	mV	At V <sub>DD</sub> = 3.3V
Power Supply Current	I <sub>DD</sub>	ı	245	550	mA	Note 1
Positive-going Input Threshold Voltage	V <sub>IT+</sub>	1	í	100	mV	\/ = 1.2\/ tvp
Negative-going Input Threshold Voltage	V <sub>IT-</sub>	-100	-	-	mV	V <sub>cm</sub> = 1.2V typ.
Differential Input Voltage	V <sub>ID</sub>	200	-	600	mV	
	P <sub>D</sub>	-	0.8	1.82	W	Note 1
Power Consumption	P <sub>BL</sub>	-	-	2.6	W	Note 2
	P <sub>total</sub>	-	-	4.42	W	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for 3.3V at 25 ℃.

a) Typ: Mosaic Pattern 8\*8b) Max: skip subpixel 255

2. Calculated value for reference (VLED  $\times$  ILED)

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# 3.2 Backlight Unit

< Table 4. LED Driving guideline specifications >

Ta=25+/-2°C

	Parameter		Min.	Тур.	Max.	Unit	Remarks
LED Forward	Voltage	V <sub>F</sub>	-	1	3.0	V	-
LED Forward	Current	I <sub>F</sub>	-	20.3		mA	-
LED Power C	Consumption	P <sub>LED</sub>		-	2.6	◆ W	Note 1
LED Life-Tim	е	N/A	15,000	-		Hour	I <sub>F</sub> = 20.3mA
Power supply voltage for LED Driver		V <sub>LED</sub>	5	12	20	V	
EN Control	Backlight on		2.5		5.0	V	
Level	Backlight off		0		1.0	V	
PWM Control	PWM High Level		2.5		5.0	٧	
Level	PWM Low Level		0		0.1	V	
PWM Control Frequency		F <sub>PWM</sub>	200	ı	10,000	Hz	
Duty Ratio		-	1	-	100	%	Note3

Notes: 1. Power supply voltage12V for LED Driver,

Calculator Value for reference IF  $\times$  VF  $\times$  36 /Driver efficiency = PLED

- 2. The LED Life-time define as the estimated time to 50% degradation of initial luminous.
- 3. 1% duty cycle is achievable with a dimming frequency less than 1KHz.

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# 4.0 OPTICAL SPECIFICATION

#### 4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance  $\leq 1$  lux and temperature =  $25\pm2^{\circ}$ C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of  $\theta$  and  $\Phi$  equal to  $0^{\circ}$ . We refer to  $\theta\emptyset=0$  (= $\theta3$ ) as the 3 o'clock direction (the "right"),  $\theta\emptyset=90$  (= $\theta12$ ) as the 12 o'clock direction ("upward"),  $\theta\emptyset=180$  (= $\theta9$ ) as the 9 o'clock direction ("left") and  $\theta\emptyset=270$ (= $\theta6$ ) as the 6 o'clock direction ("bottom"). While scanning  $\theta$  and/or  $\emptyset$ , the center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be 3.3+/-0.3V at  $25^{\circ}$ C. Optimum viewing angle direction is 6 'clock.

# 4.2 Optical Specifications

<Table 5. Optical Specifications>

Table 6. Optical openications								
Paramo	eter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
	Harizantal	$\Theta_3$		40	45	-	Deg.	
Viewing Angle	Horizontal	$\Theta_9$	CR > 10	40	45	1	Deg.	Note 1
range	Vertical	Θ <sub>12</sub>	CR > 10	15	20	-	Deg.	INOLE I
	Vertical	$\Theta_6$		30	40	-	Deg.	
Luminance Co	ntrast ratio	CR	Θ = 0°	400	500			Note 2
Luminance of White	5 Points	Y <sub>w</sub>	Θ = 0°	187	220	-	cd/m <sup>2</sup>	Note 3
White	5 Points	ΔΥ5	ILED = 20.3mA	80	-	-		
Luminance uniformity 13 Poi	13 Points	ΔΥ13		60	-	-		Note 4
White Chro	maticity	X <sub>w</sub>	Θ = 0°	0.283	0.313	0.343		Note 5
write Cillo	maticity	$y_w$	0 = 0	0.299	0.329	0.359		Note 5
	Red	$X_R$			0.590			
	rteu	y <sub>R</sub>			0.350			
Reproduction	Green	$X_{G}$	0 00	0.00	0.330	. 0. 00		
of color	Green	$y_{G}$	Θ = 0°	-0.03	0.555	+0.03		
	Dlus	X <sub>B</sub>			0.153			
	Blue	y <sub>B</sub>			0.119			
Response (Rising + F		T <sub>RT</sub>	Ta= 25° C Θ = 0°	-	12	16	ms	Note 6
Cross 7	alk	CT	Θ = 0°	-	-	2.0	%	Note 7

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### Notes:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).
- 2. Contrast measurements shall be made at viewing angle of  $\Theta$ = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state.

(see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

CR = Luminance when displaying a white raster

Luminance when displaying a black raster

- 3. Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as :  $\Delta Y$  =Minimum Luminance of 5(or 13) points / Maximum Luminance of 5(or 13) points. (see FIGURE 2 and FIGURE 3).
- 5. The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See FIGURE 5).

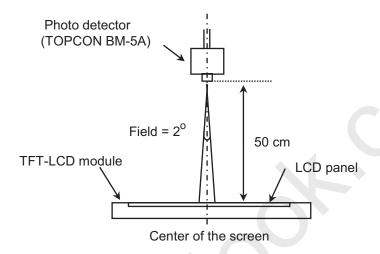
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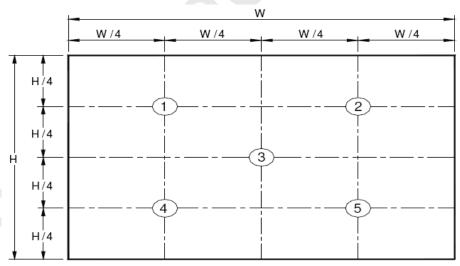
# 4.3 Optical measurements

Figure 1. Measurement Set Up



Optical characteristics measurement setup

Figure 2. White Luminance and Uniformity Measurement Locations (5 points)



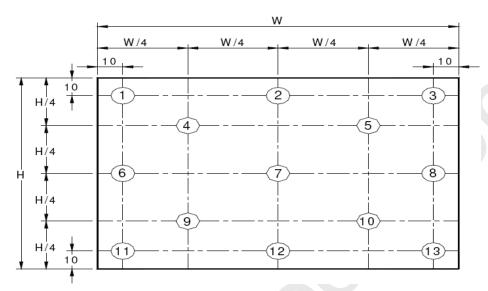
Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

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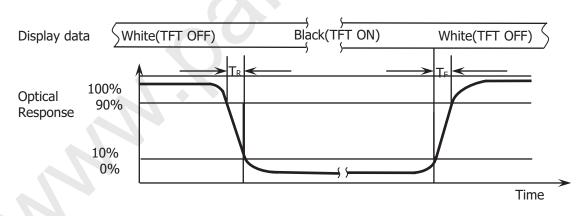
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Figure 3. Uniformity Measurement Locations (13 points)



The White luminance uniformity on LCD surface is then expressed as :  $\Delta Y5 = Minimum Luminance of five points / Maximum Luminance of five points (see FIGURE 2), <math>\Delta Y13 = Minimum Luminance of 13 points / Maximum Luminance of 13 points (see FIGURE 3).$ 

Figure 4. Response Time Testing



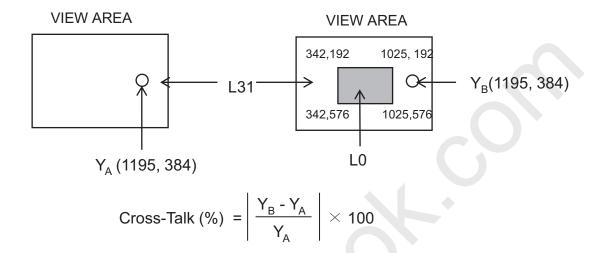
The electro-optical response time measurements shall be made as shown in FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td and 90% to 10% is Tr.

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Figure 5. Cross Modulation Test Description



Where:

 ${
m Y_A}$  = Initial luminance of measured area (cd/m²)  ${
m Y_B}$  = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark (Refer to FIGURE 5).

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## 5.0 INTERFACE CONNECTION.

### **5.1 Electrical Interface Connection**

The electronics interface connector is UJU IS050-L30B-C10 or Compatible. The connector interface pin assignments are listed in Table 6.

< Table 6 Pin Assignments for the Interface Connectors

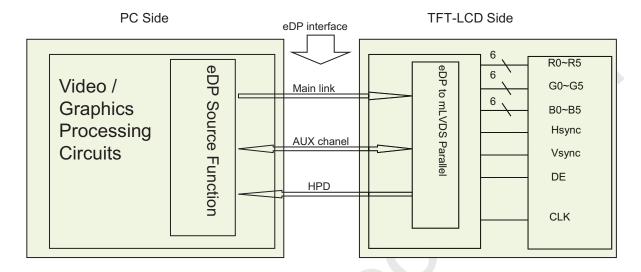
	<table 6.="" assignments="" connector="" for="" interface="" pin="" the=""></table>		
Terminal	Symbol	Functions	
Pin No.	Symbol	Description	
1	NC	No Connection	
2	H_GND	Ground	
3	LANE1_N	eDP RX channel 1 negative	
4	LANE1_P	eDP RX channel 1 positive	
5	H_GND	Ground	
6	LANE0_N	eDP RX channel 0 negative	
7	LANE0_P	eDP RX channel 0 positive	
8	H_GND	Ground	
9	AUX_CH_P	eDP AUX CH positive	
10	AUX_CH_N	eDP AUX CH negative	
11	H_GND	Ground	
12	LCD_VCC	Power Supply, 3.3V (typ.)	
13	LCD_VCC	Power Supply, 3.3V (typ.)	
14	LCD_Self_Test	Panel self test enable	
15	H_GND	Ground	
16	H_GND	Ground	
17	HPD	Hot plug detect output	
18	BL_GND	LED Ground	
19	BL_GND	LED Ground	
20	BL_GND	LED Ground	
21	BL_GND	LED Ground	
22	BL_ENABLE	LED enable pin(+3.3V Input)	
23	BL_PWM	System PWM Signal Input	
24	NC	Reserved for LCD manufacturer"s use	
25	NC	Reserved for LCD manufacturer"s use	
26	BL_POWER	LED Power Supply 5V-21V	
27	BL_POWER	LED Power Supply 5V-21V	
28	BL_POWER	LED Power Supply 5V-21V	
29	BL_POWER	LED Power Supply 5V-21V	
30	NC	No Connection	

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# 5-2. eDP Interface



Note. Transmitter : Parade DP501or equivalent.

Transmitter is not contained in Module.

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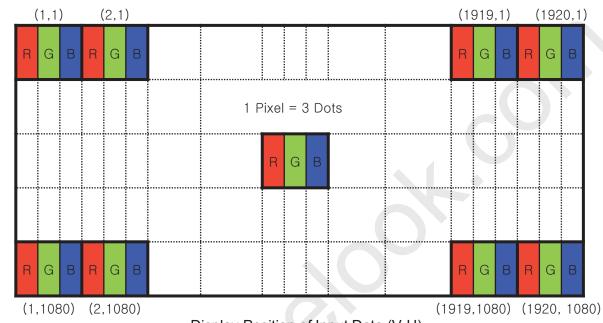
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# 5.3 Data Input Format

<Table 6. Pin Assignments for the Interface Connector>



Display Position of Input Data (V-H)

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# **6.0 SIGNAL TIMING SPECIFICATION**

# 6.1 The NT156FHM-N41 is operated by the DE only.

Item		Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	91.5	141.4	152.8	MHz
Clock	High Time	Tch	-	4/7Tc	-	Tc
	Low Time	Tcl	-	4/7Tc	1	Tc
Frame Period			1100	1100	1120	lines
		eriod Tv		60	62	Hz
			25	16.67	16.13	ms
Vertical Display Period		Tvd	-	1080	1	lines
One line Scanning Period		Th	2080	2142	2200	clocks
Horizontal Display Period		Thd	<u></u>	1920	-	clocks

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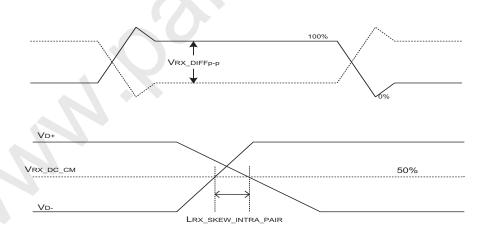
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# **6.2 eDP Rx Interface Timing Parameter**

The specification of the eDP Rx interface timing parameter is shown in Table 8.

<Table 8. eDP Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
Spread spectrum clock	ssc		0.5		%	
Differential peak-to-peak input volt age at package pins	VRX-DIFFp-p	100	0	1320	mV	
Rx input DC common mode voltage	VRX_DC_CM	-	GND	-	V	
Differential termination resistance	RRX-DIFF	80	-	100	Ω	
Single-ended termination resistance	RRX-SE	40	-	60	Ω	
Rx short circuit current limit	IRX_SHORT	-	-	20	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_ INTRA_PAIR			150	ps	



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# 7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

	Colors &	Data signal			
	Gray scale	R0 R1 R2 R3 R4 R5	G0 G1 G2 G3 G4 G5	B0 B1 B2 B3 B4 B5	
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0	
	Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1	
Basic	Green	0 0 0 0 0 0	1 1 1 1 1 1	0 0 0 0 0 0	
colors	Light Blue	0 0 0 0 0 0	1 1 1 1 1 1	1 1 1 1 1 1	
	Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0	
	Purple	1 1 1 1 1 1	0 0 0 0 0 0	1 1 1 1 1 1	
	Yellow	1 1 1 1 1 1	1 1 1 1 1 1	0 0 0 0 0 0	
	White	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1	
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0	
	Δ	1 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0	
	Darker	0 1 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0	
Gray scale	$\triangle$	<u>†</u>	<u> </u>	<u>↑</u>	
of Red	$\nabla$	<u> </u>	1	<b>\</b>	
	Brighter	1 0 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0	
	abla	0 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0	
	Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0	
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0	
		0 0 0 0 0 0	1 0 0 0 0 0	0 0 0 0 0 0	
0	Darker	0 0 0 0 0 0	0 1 0 0 0 0	0 0 0 0 0 0	
Gray scale of Green	$\triangle$			Î	
or Green	Brighter	0 0 0 0 0 0	1 0 1 1 1 1	0 0 0 0 0	
		0 0 0 0 0 0	0 1 1 1 1 1	0 0 0 0 0 0	
	Green	0 0 0 0 0	1 1 1 1 1	0 0 0 0 0 0	
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0	
	Δ	0 0 0 0 0 0	0 0 0 0 0	1 0 0 0 0 0	
	Darker	0 0 0 0 0 0	0 0 0 0 0	0 1 0 0 0 0	
Gray scale	Δ	1	J.	1	
of Blue	$\nabla$	<b>\</b>		<b>1</b>	
	Brighter	0 0 0 0 0 0	0 0 0 0 0 0	1 0 1 1 1 1	
	$\nabla$	0 0 0 0 0 0	0 0 0 0 0 0	0 1 1 1 1 1	
	Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1	
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0	
Gray	Δ	1 0 0 0 0 0	1 0 0 0 0 0	1 0 0 0 0 0	
scale	Darker	0 1 0 0 0 0	0 1 0 0 0 0	0 1 0 0 0 0	
of	Δ	<u> </u>	<u> </u>	<u>↑</u>	
White	$\nabla$	<u> </u>	<u> </u>	<u> </u>	
&	Brighter	1 0 1 1 1 1	1 0 1 1 1 1	1 0 1 1 1 1	
Black	$\nabla$	0 1 1 1 1 1	0 1 1 1 1 1	0 1 1 1 1 1	
	White	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1	

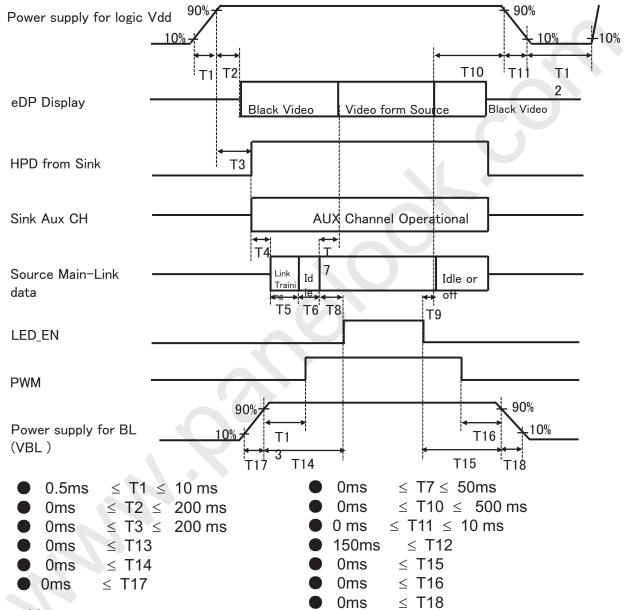
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# 8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off seq uence shall be as shown in below



### Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

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# 9.0 Connector Description

Physical interface is described as for the connector on LCM.

These connectors are capable of accommodating the following signals and will be following components.

### 9.1 TFT LCD Module

For Signal Connector
UJU or Compatible
IS050-L30B-C10 or Compatible
I-PEX 20454-030T or Compatible

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### 10.0 MECHANICAL CHARACTERISTICS

### 10.1 Dimensional Requirements

FIGURE 6 shows mechanical outlines for the model NT156FHM-N41. Other parameters are shown in Table 9.

<Table 9. Dimensional Parameters>

Parameter	Specification	Unit
Active Area	344.16 (H) ×193.59 (V)	
Number of pixels	1920 (H) X 1080 (V) (1 pixel = R + G + B dots)	
Pixel pitch	0.17925 (H) X 0.17925 (V)	
Pixel arrangement	RGB Vertical stripe	
Display colors	262K	
Display mode	Normally white	
Dimensional outline	359.5(H)*223.8(V) (W/PCB)*3.2(Max) 359.5 (H)×206.5(V)×3.2 (max)	mm
Weight	370 (max)	gram

### 10.2 Mounting

See FIGURE 6.

### 10.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an AG coating to minimize reflection and a coating to reduce sc ratching.

### 10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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# 11.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

<Table 10. Reliability test>

No	Test Items	Conditions
1	High temperature storage test	Ta = 60 ℃, 240 hrs
2	Low temperature storage test	Ta = -20 ℃, 240 hrs
3	High temperature & high humidity operation test	Ta = 50 ℃, 80%RH, 240 hrs
4	High temperature operation test	Ta = 50 ℃, 240 hrs
5	Low temperature operation test	Ta = 0 °C, 240 hrs
6	Thermal shock	Ta = -20 $^{\circ}$ C $\leftrightarrow$ 60 $^{\circ}$ C (0.5 hr), 100 cycle
7	Vibration test (non-operating)	1.5G, 10~500Hz,Half Sine X,Y,Z / Sweep rate : 1 hour
8	Shock test (non-operating)	220G, Half Sine Wave 2msec $\pm$ X, $\pm$ Y, $\pm$ Z Once for each direction
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV

### 12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
  - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
  - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
  - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
  - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
  - Do not pull the interface connector in or out while the LCD module is operating.
  - Put the module display side down on a flat horizontal plane.
  - Handle connectors and cables with care.
- (3) Cautions for the operation
  - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
  - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

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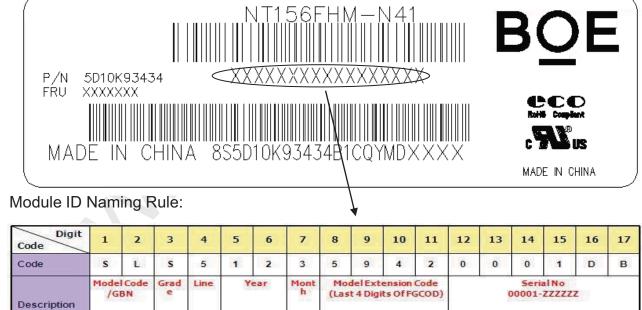


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- (4) Cautions for the atmosphere
  - Dew drop atmosphere should be avoided.
  - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
  - Do not apply fixed pattern data signal to the LCD module at product aging.
  - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
  - Do not disassemble and/or re-assemble LCD module.
  - Do not re-adjust variable resistor or switch etc.
  - When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

### **13.0 LABEL**





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# (2) High voltage caution label



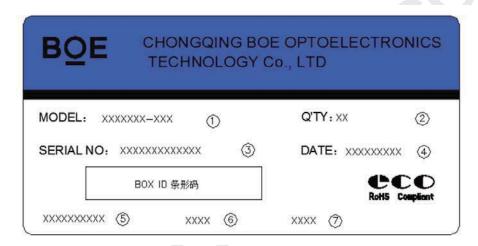
### HIGH VOLTAGE CAUTION

RISK OF ELECTRIC SHOCK, DISCONNECT THE ELECTRIC POWER BEFORE SERVICING

COLD CATHODE FLUORESCENT LAMP IN LCD
PANEL CONTAINS A SMALL AMOUNT

OF MERCURY, PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR DISPOSAL.

### (3) Box label



# 序列号标注部分需打印, 说明如下:

- 1. FG-CODE(前12位)
- 2. 产品数量

3. Box ID

- 4. 包装日期
- 5. 客户端段物料号(客户端)---暂不打印
- 6. FG-Code后四位
- 7. 供应商代码 ---暂不打印
- 8. Total Size:100×50mm

Digit Code	1	2	3	4	5	6	7	8	9	10	11	12	13
Code	S	L	S	5	1	2	3	D	0	0	0	6	8
Description	Produc	ts GBN	Grade	Line		ar	Month	Revisio n Code			alNo		

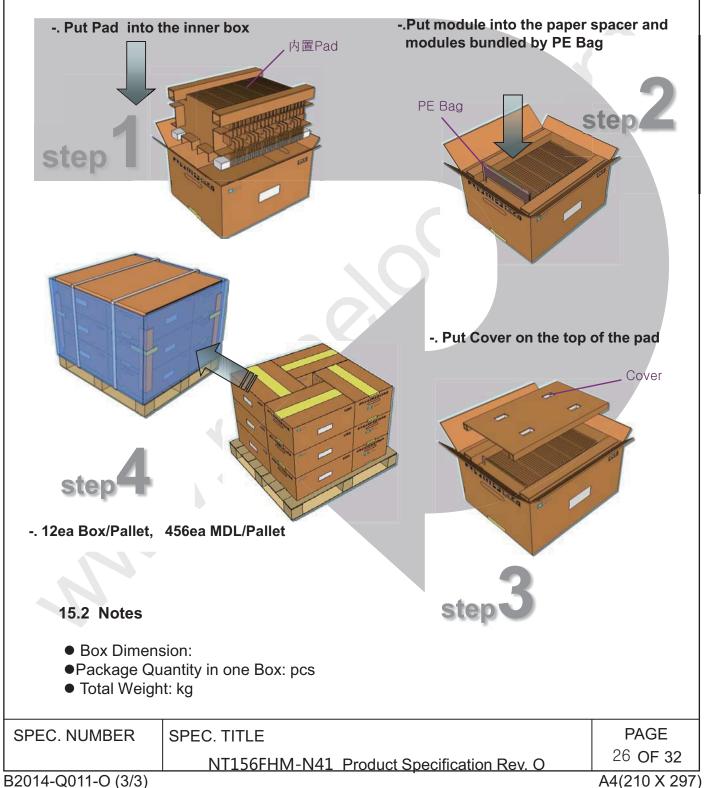
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# 14.0 PACKING INFORMATION

# 15.1 Packing order

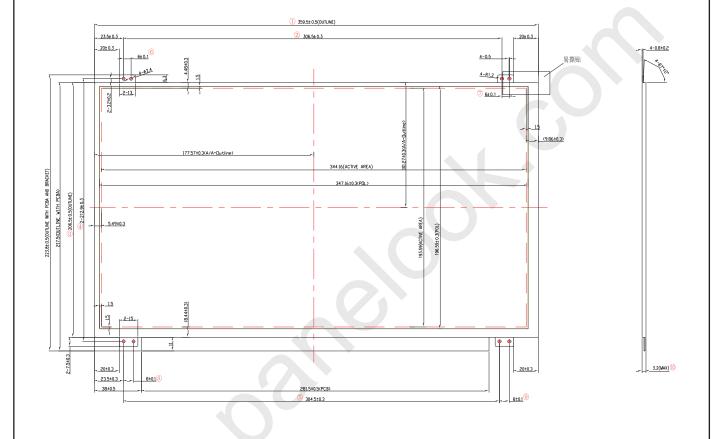




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# 15.0 MECHANICAL OUTLINE DIMENSION

Figure 6. TFT-LCD Module Outline Dimension (Front View)



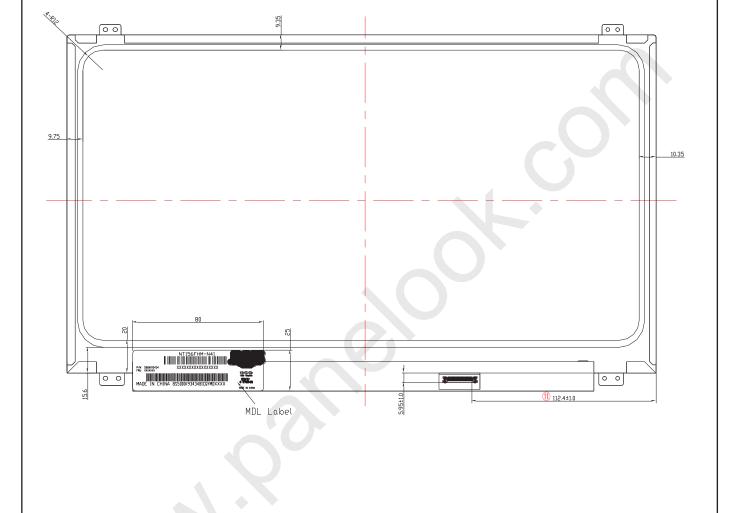
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Figure 7. TFT-LCD Module Outline Dimensions (Rear view)



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# 16.0 EDID Table

Address	Function	Hex		Dec	Input values.	Notes
(HEX)						
00		00	0		0	
01		FF	255		255	
02		FF	255		255	
03	Header	FF	255		255	EDID Header
04	ricadei	FF	255		255	LDID Ficuaci
05		FF	255		255	
06		FF	255		255	
07		00	0		0	
08	ID Manufacturer	09	9		BOE	ID = BOE
09	Name	E5	229		BOE	ID - BOE
0A	ID Product Code	BA	186		1722	ID = 1722
0B	1D Product Code	06	6		1722	ID = 1/22
0C		00	0			
0D	32-bit serial No.	00	0			
0E	32-DIL Serial NO.	00	0			
0F		00	0			
10	Week of manufacture	01	1		1	
11	Year of Manufacture	1A	26		2016	Manufactured in 2016
12	EDID Structure Ver.	01	1		1	EDID Ver 1.0
13	EDID revision #	04	4		4	EDID Rev. 0.4
14	Video input definition	95	149		-	digital signal/DP input
15	Max H image size	22	34		34	34 cm (Approx)
16	Max V image size	13	19		19	19 cm (Approx)
17	Display Gamma	78	120		2.2	Gamma curve = 2.2
18	Feature support	02	2			RGB display, Preferred Timming mode/RGB 4:4:4
19	Red/Green low bits	В0	176		-	Red / Green Low Bits
1A	Blue/White low bits	90	144		-	Blue / White Low Bits
1B	Red x high bits	97	151	606	0.592	Red(x) = 10010111(0.592)
1C	Red y high bits	58	88	355	0.347	Red(y) = 01011000(0.347)
1D	Green x high bits	54	84	336	0.329	Green $(x) = 01010100 (0.329)$
1E	Green y high bits	92	146	584	0.571	Green (y) = 10010010 (0.571)
1F	Blue x high bits	26	38	154	0.151	Blue $(x) = 00100110 (0.151)$
20	BLue y high bits	1D	29	117	0.115	Blue (y) = 00011101 (0.115)
21	White x high bits	50	80	320	0.313	White $(x) = 01010000 (0.313)$
22	White y high bits	54	84	336	0.329	White $(y) = 01010100 (0.329)$
23	Established timing 1	00	0		-	
24	Established timing 2	00	0		-	

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25	Established timing 3	00	0	-	
26		01	1		
27	Standard timing #1	01	1		Not Used
28		01	1		
29	Standard timing #2	01	1		Not Used
2A	G. 1 1	01	1		
2B	Standard timing #3	01	1		Not Used
2C	Chandend bineine #4	01	1		Netterd
2D	Standard timing #4	01	1		Not Used
2E	Chandaud timina #F	01	1		Natilland
2F	Standard timing #5	01	1		Not Used
30	Standard timing #6	01	1		Not Used
31	Standard timing #6	01	1		Not Osed
32	Standard timing #7	01	1		Not Used
33	Standard tilling #7	01	1		Not used
34	Standard timing #8	01	1		Not Used
35	Standard tirling #0	01	1		Not used
36		3C	60	141.4	141.4MHz Main clock
37		37	55	111.1	TTI. IPHIZ PIGHT CIOCK
38		80	128	1920	Hor Active = 1920
39		DE	222	222	Hor Blanking = 222
3A		70	112	-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B		38	56	1080	Ver Active = 1080
3C		14	20	20	Ver Blanking = 20
3D		40	64	-	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E	Detailed timing/monitor	30	48	48	Hor Sync Offset = 48
3F	descriptor #1	20	32	32	H Sync Pulse Width = 32
40		36	54	3	V sync Offset = 3 line
41		00	0	6	V Sync Pulse width: 6 line
42		58	88	344	Horizontal Image Size = 344 mm (Low 8 bits)
43		C1	193	193	Vertical Image Size = 193 mm (Low 8 bits)
44		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45		00	0	0	Hor Border (pixels)
46		00	0	0	Vertical Border (Lines)
47		1A	26		Refer to right table

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40		00			
48	_	00	0	0.0	0MHz Main clock
49		00	0		
4A		00	0	0	Hor Active = 0
4B		00	0	0	Hor Blanking = 0
4C		00	0	-	4 bits of Hor. Active + 4 bits of Hor.  Blanking
4D		00	0	0	Ver Active = 768
4E		00	0	0	Ver Blanking = 0
4F	Detailed	00	0	-	4 bits of Ver. Active + 4 bits of Ver.  Blanking
50	timing/monitor	00	0	0	Hor Sync Offset = 0
51	descriptor #2	00	0	0	H Sync Pulse Width = 0
52		00	0	0	V sync Offset = 0 line
53		00	0	0	V Sync Pulse width: 0 line
54		00	0	0	Horizontal Image Size = 0 mm (Low 8 bits)
55		00	0	0	Vertical Image Size = 0 mm (Low 8 bits)
56		00	0	-	4 bits of Hor Image Size + 4 bits of Ver Image Size
57		00	0	0	Hor Border (pixels)
58		00	0	0	Vertical Border (Lines)
59		00	0		
5A		00	0		
5B		00	0		
5C		00	0		ASCII Data Sting Tag
5D		FE	254		
5E		00	0		
5F		42	66	В	
60		4F	79	0	
61		45	69	Е	
62	Detailed	20	32		
63	timing/monitor descriptor #3	43	67	С	
64	acscriptor #3	51	81	Q	
65		0A	10	-	Manufacture name : BOECQ
66		20	32		_
67		20	32		
68		20	32		
69		20	32		7
6A		20	32		
6B		20	32		

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6C		00	0				
6D			00	0			
6E			00	0			Product Name Tag (ASCII)
6F		FE	254				
70		00	0				
71		4E	78		N		
72		54	84		Т		
73		31	49		1		
74	Detailed	35	53		5		
75	timing/monitor descriptor #4		36	54		6	
76		46	70		F	Madal ware a NT4 ECELIM NA4	
77		48	72		Н	Model name: NT156FHM-N41	
78		4D	77		М		
79		2D	45		-		
7A		4E	78		N		
7B		34	52		4		
7C		31	49		1		
7D		0A	10				
7E	Extension flag	00	0				
7F	Checksum	FF	255	255	-		

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